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A SCALABLE CLOCK DISTRIBUTION FOR MULTIPLE CRU
ON THE SAME CHIP

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ABSTRACT OF THE DISCLOSURE

A scalable clock recovery system. In one embodiment the system comprises a clock master unit, a clock distribution network, and a plurality of clock recovery units. The master 10 clock unit generates a plurality of master clock signals, which are received by the clock recovery units. The clock recovery units use multiple stages of mixing to generate a recovered clock. The recovered clock can be used to recover data from a serial data stream.

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